Docket No.: E0196.0100

AMENDMENTS TO THE CLAIMS

1-21. (Canceled)

A method for protected transmission of data words, comprising: 22. (New)

providing a first data word;

transforming the first data word into a sequence comprising at least one second

data word using a first transformation rule,

transforming at least one of the second data words into a third data word using a

second transformation rule; and

checking whether a prescribed relationship exists between the third data word

and a comparison data word.

23. (New) The method as claimed in claim 22, further comprising executing an alarm

function if the prescribed relationship does not exist between the third data word and

the comparison data word.

24. (New) The method as claimed in claim 23, wherein the alarm function is a

function selected from the group consisting of outputting an alarm, shutting down the

circuit arrangement, shutting down and starting up the circuit arrangement, and

subjecting the third data word to repeat data processing.

25. (New) The method as claimed in claim 22, further comprising, prior to the step of

transforming at one of the second data words, modifying the at least one second data

word such that a distinct relationship exists between the third data word and the

comparison data word.

26. (New) The method as claimed in claim 25, wherein the step of modifying the at

4

least one second data word comprises adding information to the at least one second

Docket No.: E0196.0100

data word.

27. (New) The method as claimed in claim 22, wherein a distinct relationship exists

between the third data word and the comparison data word.

28. (New) The method as claimed in claim 22, wherein the distinct relationship is an

identity of the third data word with the comparison data word.

29. (New) The method as claimed in claim 22, wherein the first data wordis the

comparison data word.

30. (New) The method as claimed in claim 22, wherein the second transformation

rule is a reverse depiction of the first transformation rule.

31. (New) The method as claimed in claim 22, further comprising transforming the

first data word to produce the comparison data word by a third transformation rule.

32. (New) The method as claimed in claim 31, wherein the result of the third

transformation rule applied to the first data word is in the prescribed relationship with

the result of the application of the second transformation rule after the first

transformation rule to the first data word.

33. (New) The method as claimed in claim 31, wherein the second transformation

rule is identity and the first and third transformation rules are the same.

34. (New) A circuit arrangement for protected transmission of data words,

5

Application No. 10/572,656 (formerly (11/405,500) Amendment dated May 2, 2007

First Preliminary Amendment

comprising:

a data input connected to a first transformation device, which is configured to

Docket No.: E0196.0100

transform a first data word applied to the data input into a sequence of data words

which comprises at least one second data word;

a second transformation device, which is coupled to the first transformation

device, is configured to transform at least one of the second data words into a third data

word; and

a checking device, which has the third data word and a comparison data word

supplied thereto, is configured to check whether the third data word and the

comparison data word are in a prescribed relationship.

35. (New) The circuit arrangement as claimed in claim 34, further comprising an

alarm configured to perform an alarm function if the third data word and the

comparison data word are not in the prescribed relationship.

36. (New) The circuit arrangement as claimed in claim 35, wherein the alarm

function is a function selected from the group consisting of outputting an alarm,

shutting down the circuit arrangement, shutting down and starting up the circuit

arrangement, and subjecting the third data word to repeat data processing.

37. (New) The circuit arrangement as claimed in claim 34, wherein the first data

word is supplied to the checking device as the comparison data word.

38. (New) The circuit arrangement as claimed in claim 34, further comprising a

device configured to modify the second data word such that the prescribed relationship

between the comparison data word and the third data word is distinct.

6

Application No. 10/572,656 (formerly (11/405,500)

Amendment dated May 2, 2007

First Preliminary Amendment

The circuit arrangement as claimed in claim 34, wherein the first 39. (New)

transformation device is configured to modify the second data word such that the

Docket No.: E0196.0100

prescribed relationship between the comparison data word and the third data word is

distinct.

40. (New) The circuit arrangement as claimed in claim 34, further comprising a third

transformation device, which is connected upstream of the checking device, and is

configured to transform the first data word applied to the data input into the

comparison data word.

41. (New) The circuit arrangement as claimed in claim 40, wherein the second

transformation device is configured such that the third data word matches the second

data word.

42. (New) The circuit arrangement as claimed in claim 40, wherein the first and third

transformation devices are configured to execute the same transformation.

43. (New) The circuit arrangement as claimed in claim 34, wherein the prescribed

relationship is identity of the comparison data word and the third data word.

44. (New) The circuit arrangement as claimed in claim 34, further comprising an

arithmetic and logic unit and a memory device, wherein the first transformation device

is arranged between the arithmetic and logic unit and the memory device.

45. (New) The circuit arrangement as claimed in claim 34, further comprising at least

one further transformation device connected upstream and/or downstream of the first

transformation device.

7

Application No. 10/572,656 (formerly (11/405,500) Amendment dated May 2, 2007 First Preliminary Amendment Docket No.: E0196.0100

46. (New) The circuit arrangement as claimed in claim 34, wherein the data input is a

buffer stage.

47. (New) The circuit arrangement as claimed in claim 44, further comprising a

buffer stage, which is connected downstream of the first transformation device, and is

configured to provide the at least one second data word to the arithmetic and logic unit.

8